

IN THE SPECIFICATION:

Kindly replace the following paragraphs to correct inadvertent typographical errors:

[0053] The priority logic ~~310~~ 320 prioritizes instructions based on the order in which the instructions are arranged in the categorization queue 400. The instruction in slot0 410 has the highest priority, followed by instructions in slot1 420 and so on. The instruction in slot5 460 has the lowest priority.

[0054] Next, the priority logic ~~310~~ 320 allocates resources based on the priority of the various functional units in the DSP 100. That priority is as set forth in Table 4, below: